

Notice of References Cited	Application/Control No. 10/662,233	Applicant(s)/Patent Under Reexamination KOVER ET AL.	
	Examiner Zeev Kitov	Art Unit 2836	Page 1 of 1

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	B	US-4,216,393	08-1980	Gillberg et al.	326/88
	C	US-6,127,746	10-2000	Clemente, Stefano	307/131
	D	US-6,459,324	10-2002	Neacsu et al.	327/379
	E	US-6,271,708	08-2001	Hoshi et al.	327/377
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NON-PATENT DOCUMENTS

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	U	A. Sedra et al., Microelectronic Circuits, 1987, Holt, Reinhart and Winston, pp. 342-345, 408-410.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.